

Attorney's Docket No. 004148.P011

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)

Stephen McDonagh)

Application No.: 09/733,443)

Filed: December 7, 2000)

For: MODEL FOR CHARGE PUMP)
PHASE-LOCKED LOOP)

Examiner: Not yet assigned

Art Unit: 2631

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

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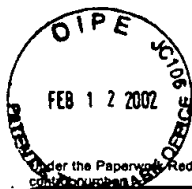
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Substitute for form 1449A/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/733,443
		Filing Date	12/07/2000
		First Named Inventor	Stephen McDonagh
		Art Unit	2631
		Examiner Name	Not yet assigned
Sheet	1	of	2
		Attorney Docket Number	004148.P011

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US- 5,490,096	02-06-1996	Seto	
	2	US- 5,987,238	11-16-1999	Chen	
	3	US-			
	4	US-			
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known

Application Number 09/733,443
Filing Date 12/07/2000
First Named Inventor Stephen McDonagh
Group Art Unit 2631
Examiner Name Not yet assigned
Attorney Docket Number 004148.P011

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OTHER PRIOR ART — NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	2
	31	International Search Report-PCT/US00/42679, November 15, 2001.	
	32	ACCO et al., Behavioral Modeling of Charge Pump Phase Locked Loops. Proceedings of the 1999 IEEE International Symposium on Circuits and Systems; June 1999, Volume 1, pages 375-378	
	34	LARSSOM, P. A Simulator Core for Charge-Pump PLLs. IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing. September 1998, Vol. 45, Issue 9, pages 1323-1326.	
	35	ARMAROLI et al. Behavioral Analysis of Charge-Pump PLLs. Proceedings of the 28th Midwest Symposium on Circuits and Systems. August 1995, Vol 2, pages 893-896	
	36	VAN PAEMEL, M. Analysis of a Charge-Pump PLL: A New Model. IEEE Transactions on Communications. July 1994, Volume 42, Issue 7, pages 2490-2498.	
	37	HOWARD, A. Applications of an Envelope Simulator. Computer-Aided Design and Test for High-Speed Electronics 52nd ARFTG Conference Digest. December 1998, pages 39-54.	
	38		
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